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CERTIFICATE OF MAILING  
37 C.F.R. § 1.8

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9/27/02  
Date

*Pam McShee*  
Signature

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
GEOFFREY S. STRONGIN

Serial No.: 09/853,226

Filed: 5/11/2001

For: MECHANISM FOR CLOSING BACK  
DOOR ACCESS MECHANISMS IN  
PERSONAL COMPUTER SYSTEMS

Group Art Unit: 2131

Examiner: Unknown

Atty. Dkt. No.: 2000.039300/TT3766

Assistant Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

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Technology Center 2100

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OCT 10 2002

OFFICE OF PETITIONS

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

RECORDED AND INDEXED  
OCT 10 2002  
U.S. PATENT AND TRADEMARK OFFICE

The present Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Assistant Commissioner is hereby authorized to deduct said fees from Williams, Morgan & Amerson, P.C., Deposit Account No. 50-0786/2000.039300/TT3766.

This application is a continuation-in-part application of both Serial Nos. 09/852,372 and 09/853,225, both filed on May 10, 2001 and both are relied upon for an earlier filing date under 35 U.S.C. § 120. In accordance with Rule 37 C.F.R. § 1.98(d) copies of the listed documents are not enclosed as they have been previously cited by or submitted to the Patent and Trademark Office in prior application Serial Nos. 09/852,372 and 09/853,225.

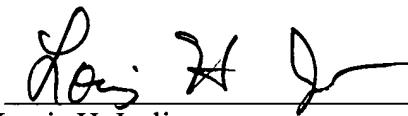
Applicant respectfully requests that the listed documents be made of record in the present case.

Applicant's additional request that the following co-pending patent applications also be considered in the present case:

<b>WMA NOS.</b>	<b>SERIAL NOS.</b>
2000.038300	09/852,372
2000.038400	09/852,942
2000.038500	09/853,395
2000.038600	09/853,446
2000.038700	09/870,447
2000.038800	09/870,889
2000.038900	09/853,225
2000.039000	09/871,084
2000.039100	09/871,511
2000.039200	09/544,858
2000.039400	09/854,040
2000.039500	09/853,465

2000.039600	09/853,443
2000.039700	09/853,437
2000.063200	09/853,335
2000.080000	09/853,234
2000.080100	09/870,890

Respectfully submitted,

  
\_\_\_\_\_  
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Date: September 27, 2002



Form PTO-1449 (modified)

 Atty. Docket No.  
2000.039300/TT3766

 Serial No.  
09/853,226

 List of Patents and Publications for Applicant's  
INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Applicant

Geoffrey S. Strongin

Filing Date:

May 11, 2001

Group:

2131

## U.S. Patent Documents

See Page 1

## Foreign Patent Documents

See Page 1

## Other Art

See Page 1

## U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1						RECEIVED
	A2						OCT 09 2002
	A3						
	A4						Technology Center 2100
	A5						

## Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						RECEIVED
	B2						
	B3						OCT OCT 10 2002
	B4						OFFICE OF PETITIONS
	B5						

## Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
	C1	Intel, "Low Pin Count (LPC) Interface Specification Revision 1.0," pp. 1-31 (09/29/97)
	C2	Standard Microsystems Corporation, "100 Pin Enhanced Super I/O for LPC Bus with SMBus Controller for Commercial Application," Part No. LPC47B37x, pp. 1-254 (06/17/99)
	C3	FIPS Pub 140-1 Federal Information Processing Standards Publication, "Security Requirements for Cryptographic Modules" (01/11/1994)
	C4	Intel, "Communication and Networking Riser Specification," Revision 1.0 (02/07/2000)
	C5	"Handbook of Applied Cryptography" CRC Press 1997 pp. 154 – 157, 160 – 161, 191 – 198, 203 – 212

EXAMINER:

DATE CONSIDERED:

EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.